Attorney Docket No.: 042390.P7462D Patent

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

j1046 U.S. PTO
09/865006
05/23/01

#4
6/28/01
M. Rodgers

In re Application of:
Parat, Krishna et al.
Serial No.: Not yet assigned
Filed: Herewith
For: A NOVEL FLASH INTEGRATED CIRCUIT AND ITS METHOD OF FABRICATION

Examiner: Not yet assigned
Art Unit: Not yet assigned

Assistant Commissioner for Patents
and Trademarks
Washington, D.C. 20231

FORMAL DRAWINGS SUBMISSION

Enclosed herewith for submission in the United States Patent and Trademark Office are twelve (12) sheets of formal drawings for the patent application referenced above.

If there are any further charges please charge them to Deposit Account No. 02-2666. A duplicate of this sheet is enclosed for that purpose.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 5/23, 2001

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Teresa Edwards
(Signature of person mailing paper or fee)

May 23, 2001
(Date signed)

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05/23/01

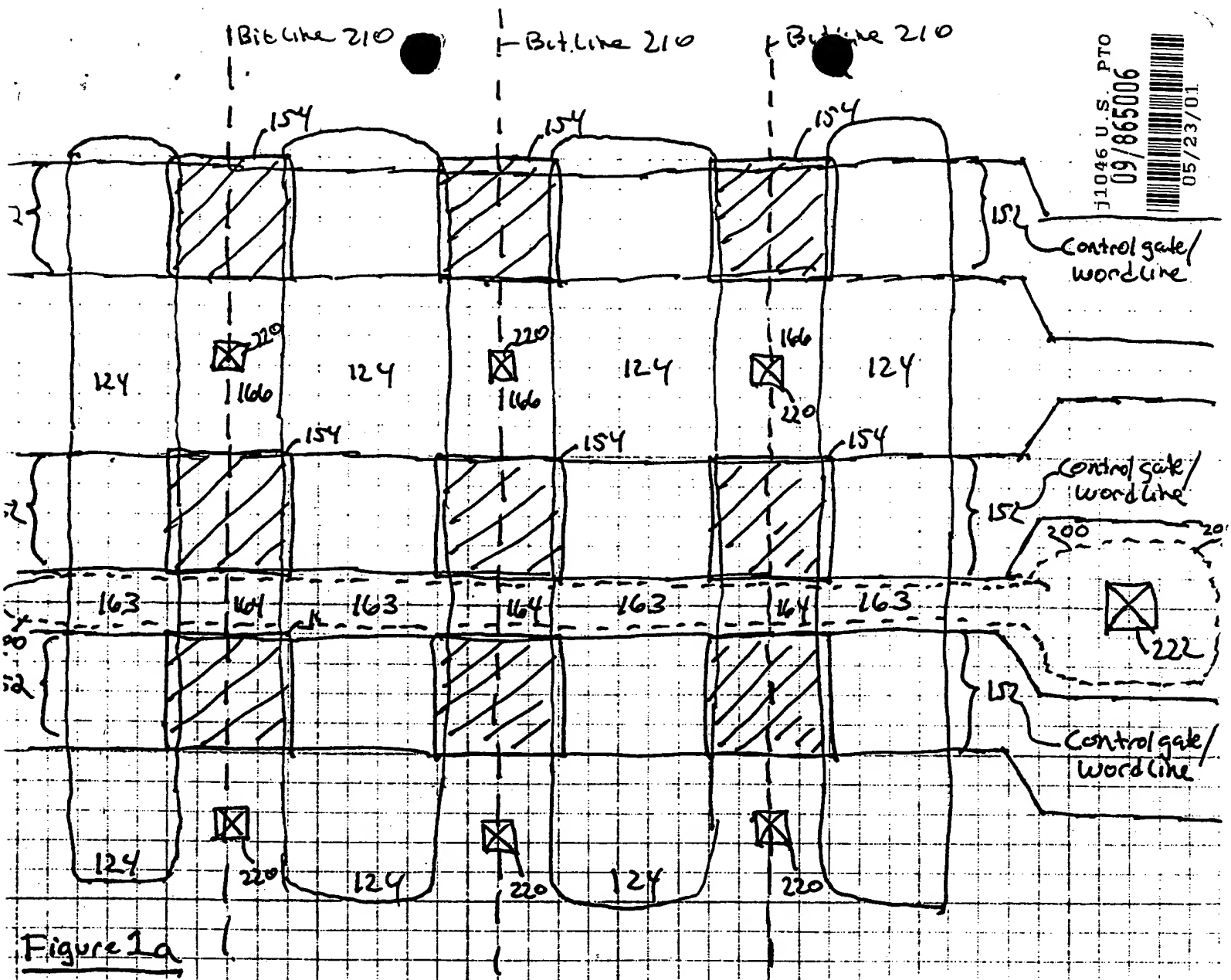


Figure 1a

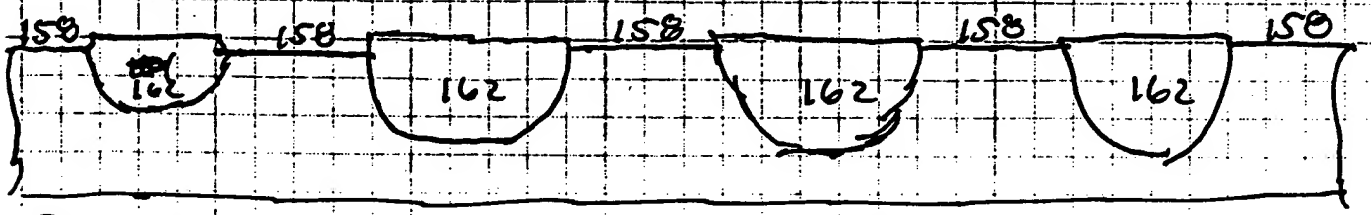


Figure 1b

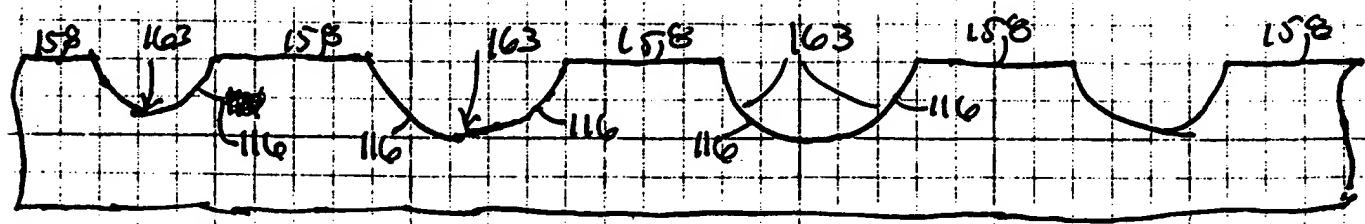


Figure 1c

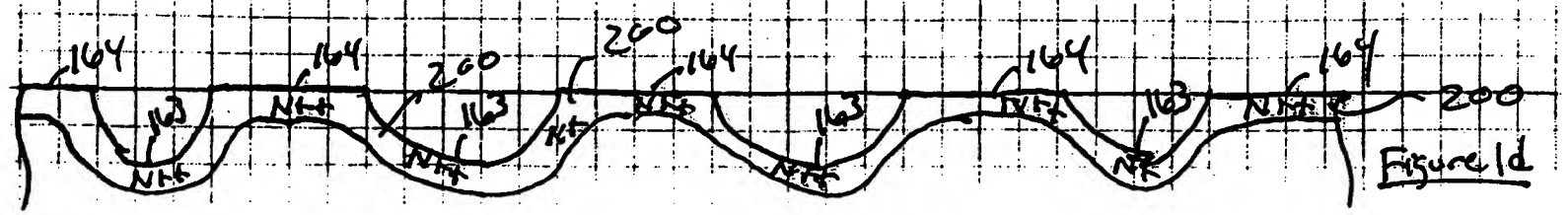
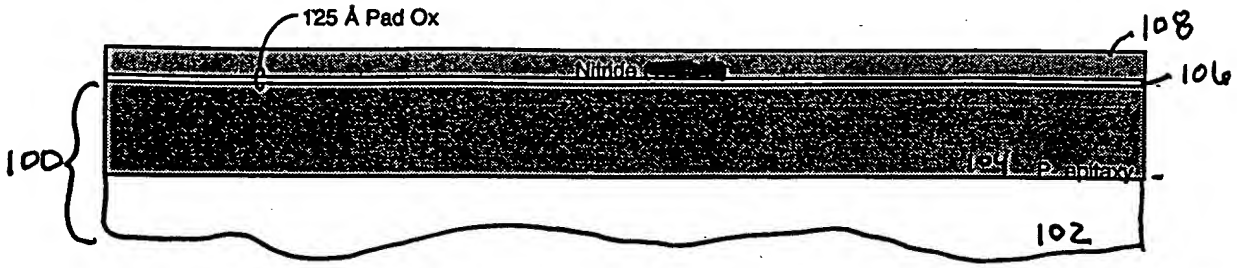


Figure 1d

Fig. 2 (along wordline direction)



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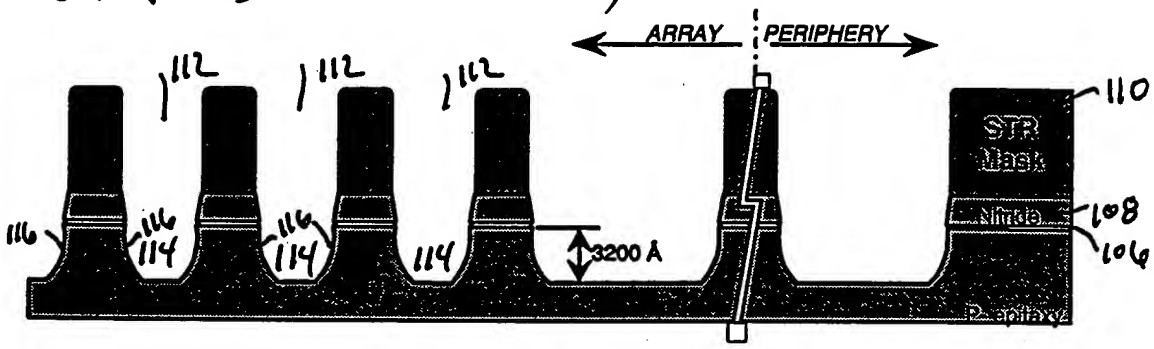

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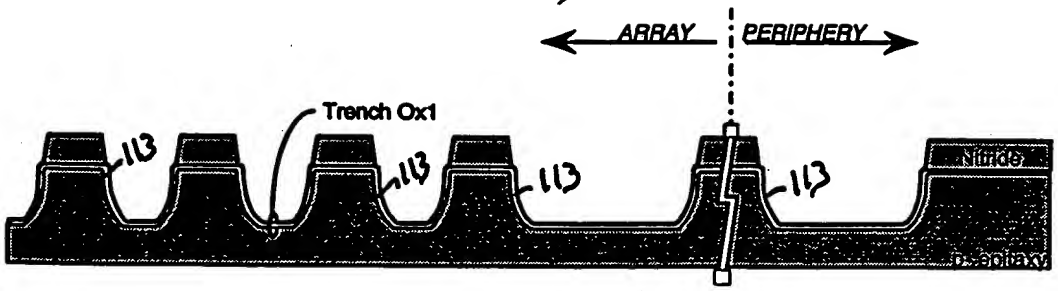

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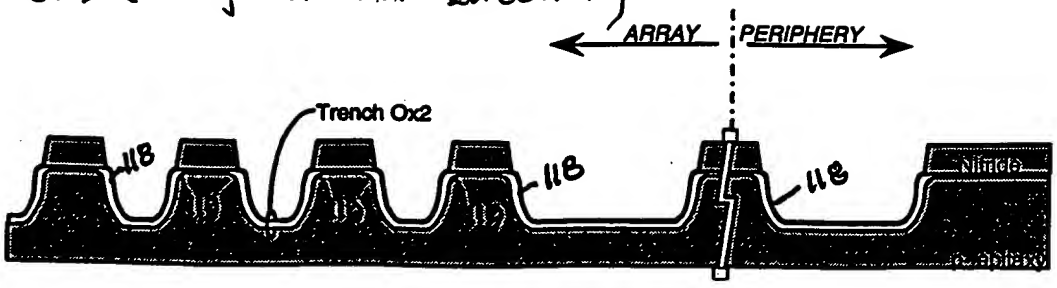

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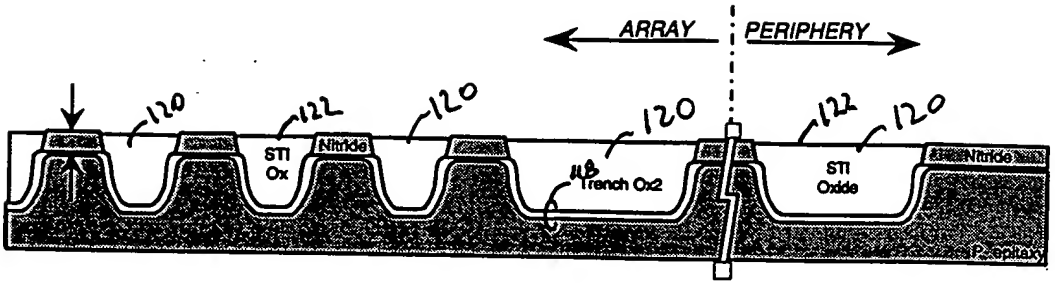

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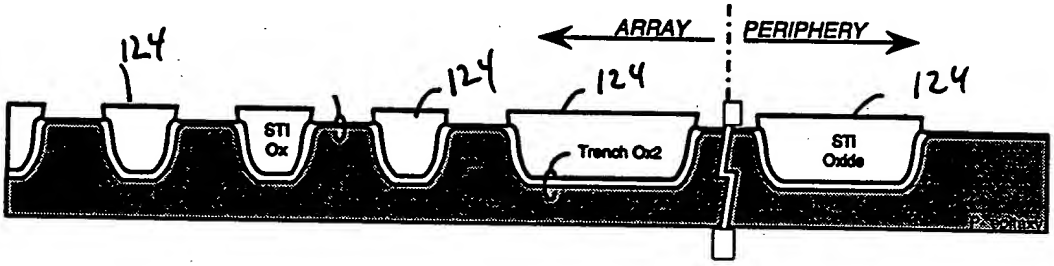

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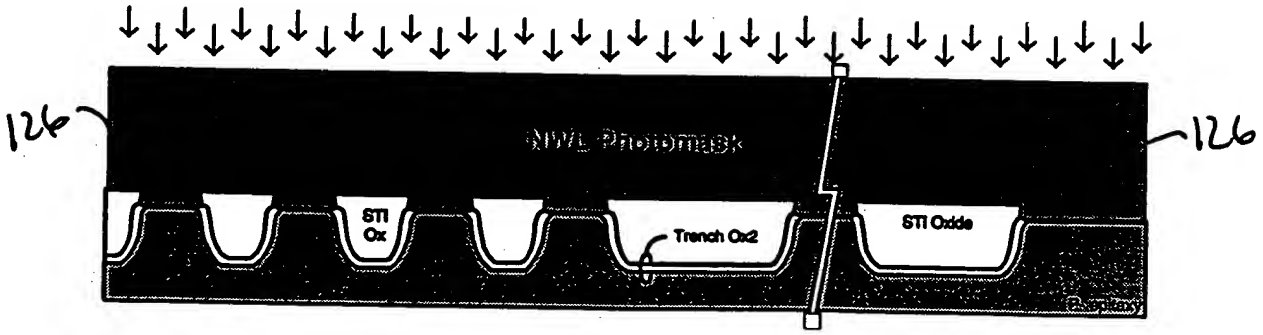

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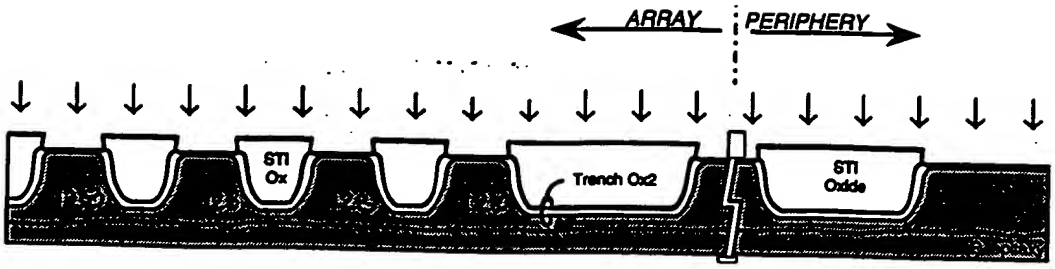

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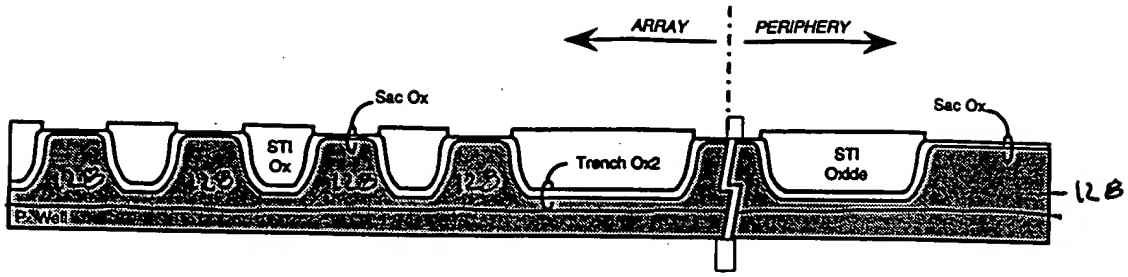

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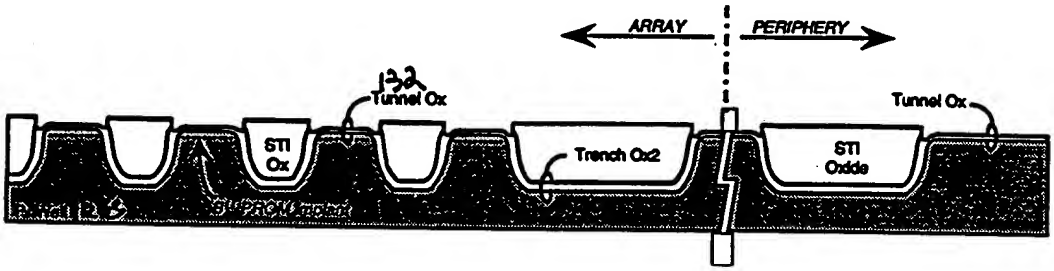

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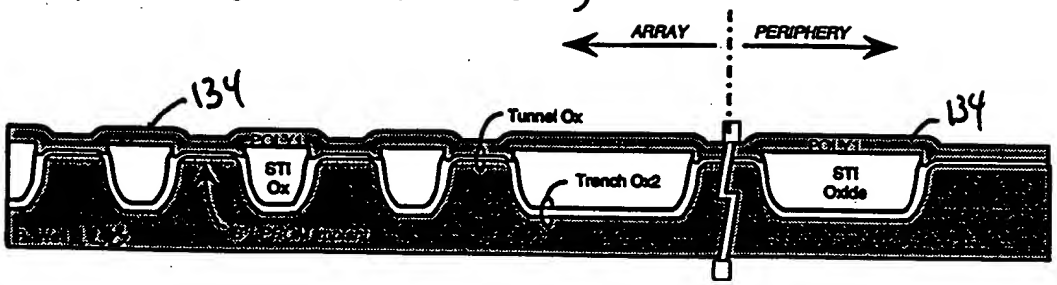

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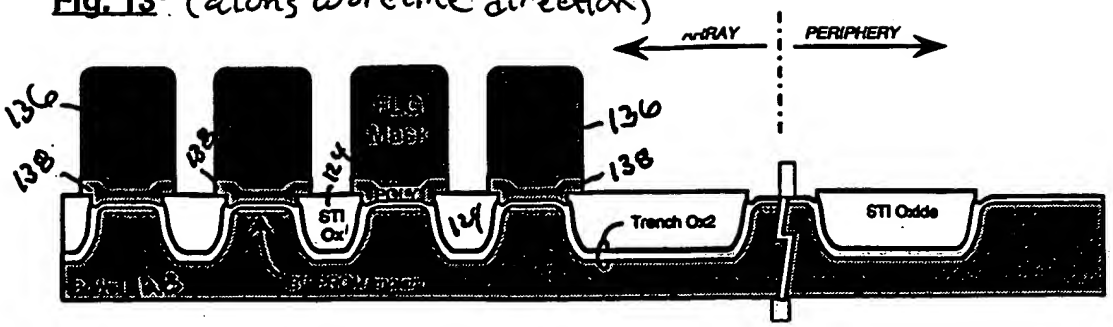

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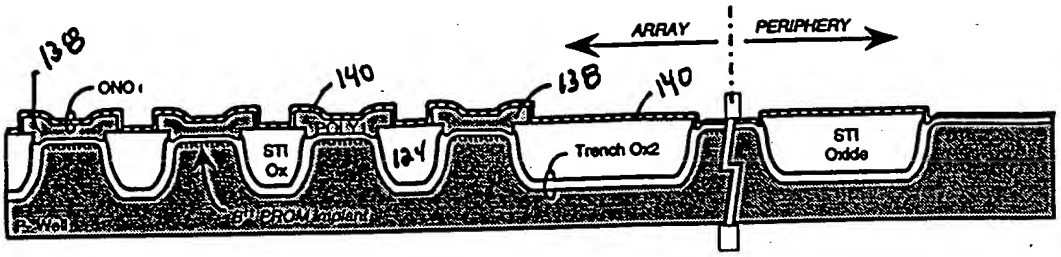

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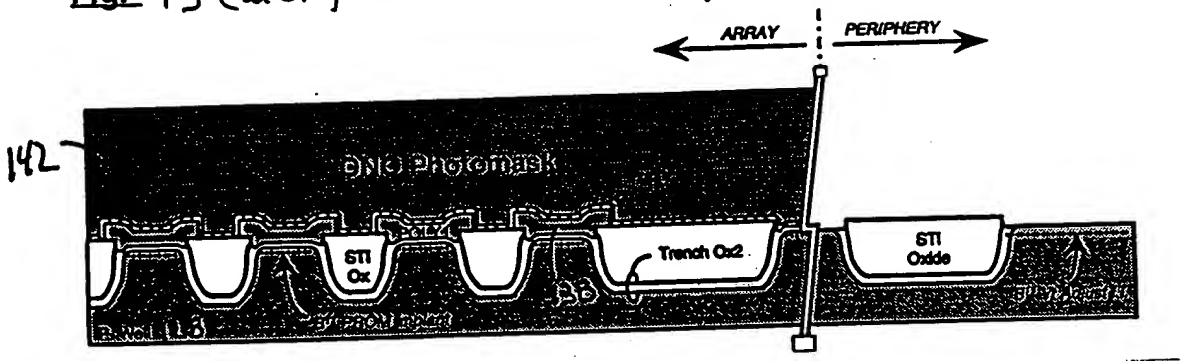

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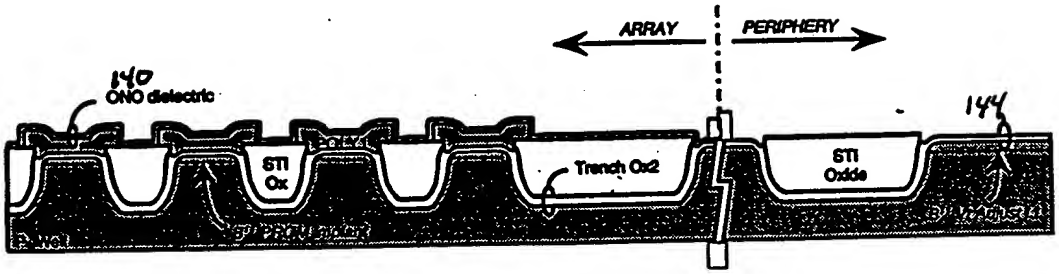

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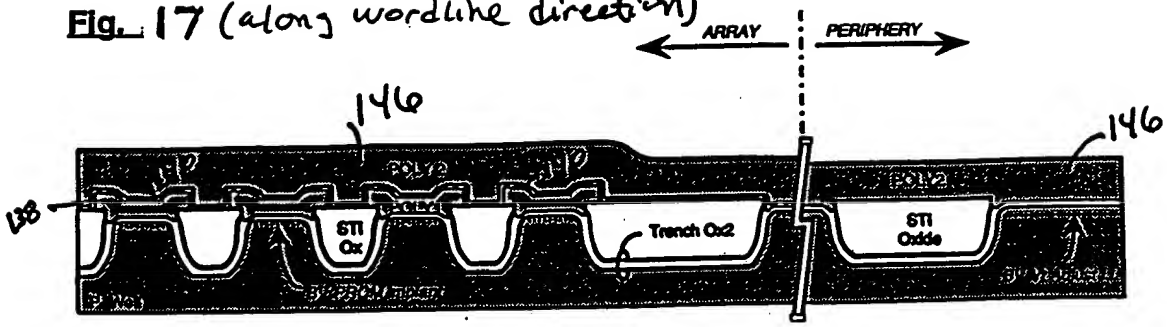

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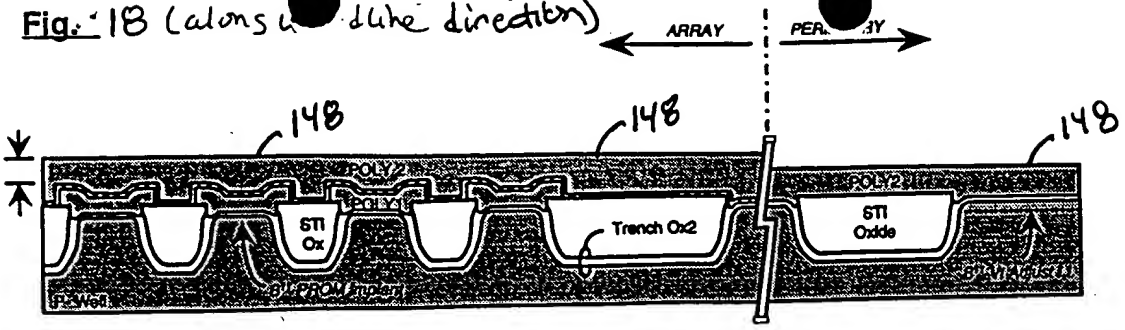


Fig. 19a (along wordline direction)

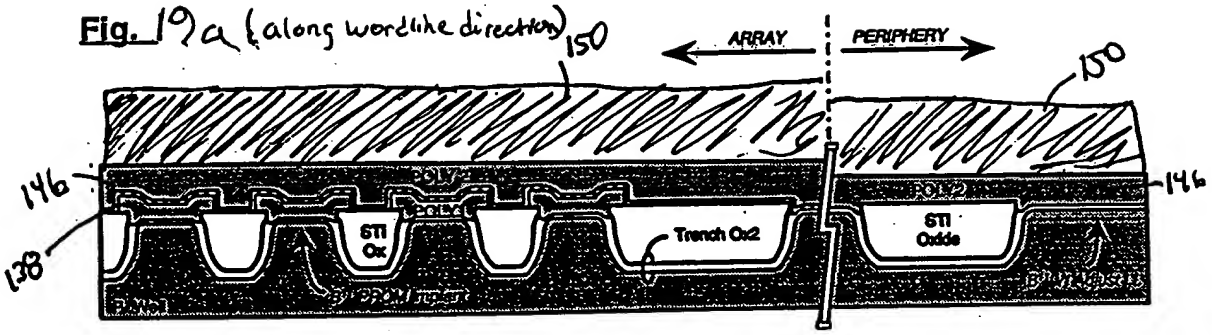


Fig. 19b

Cross-section along Bitline direction

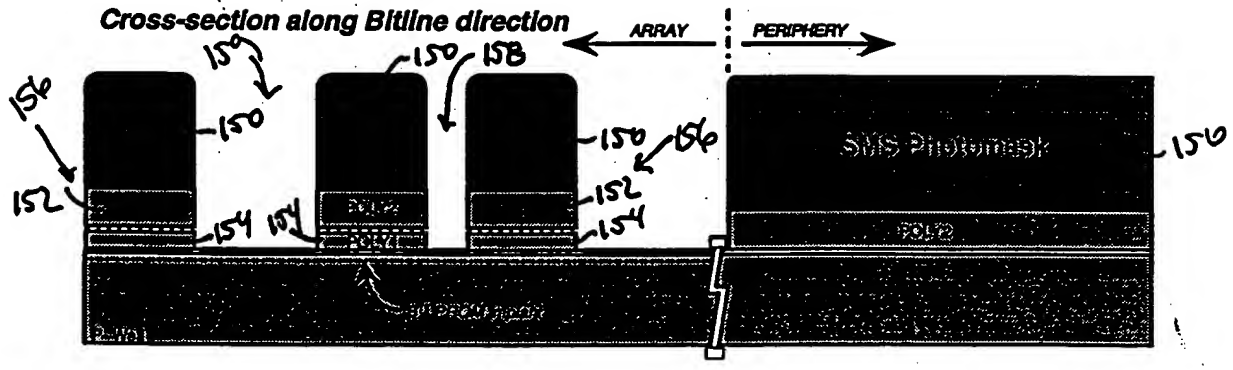


Fig. 20a thru S/D in Bit line Direction

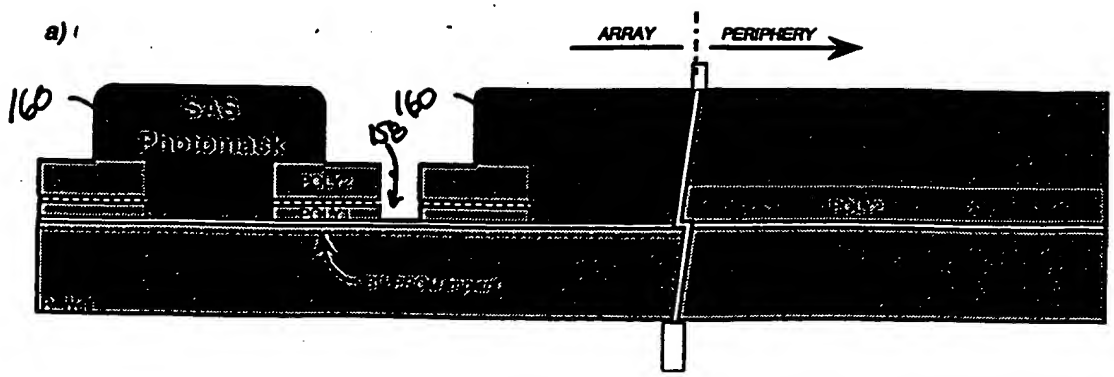


Fig 20 b) Thru STI in [redacted] line Direction

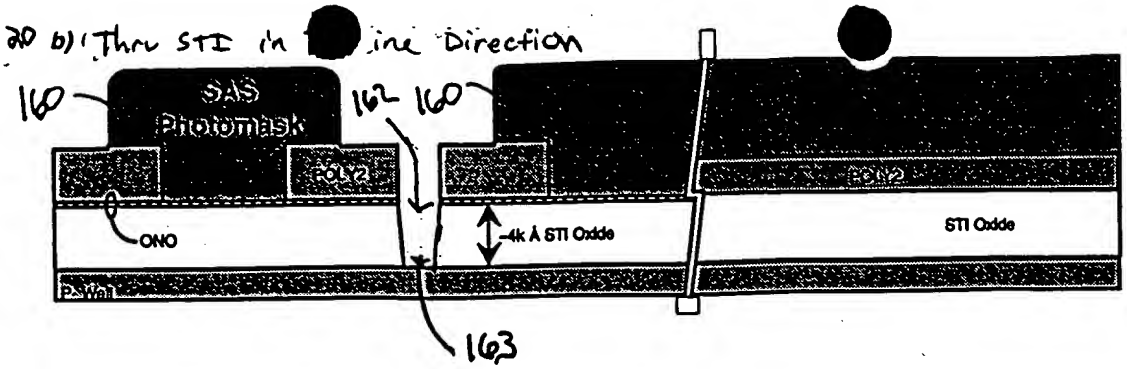
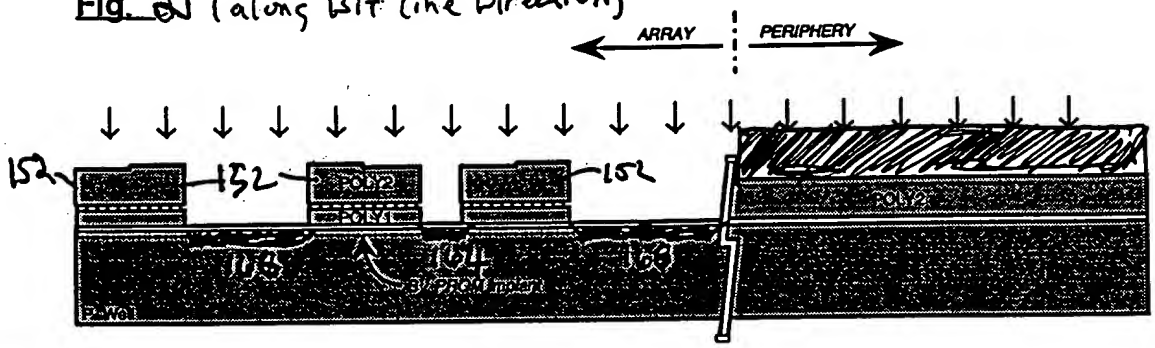


Fig. 21 (along BIT line Direction)



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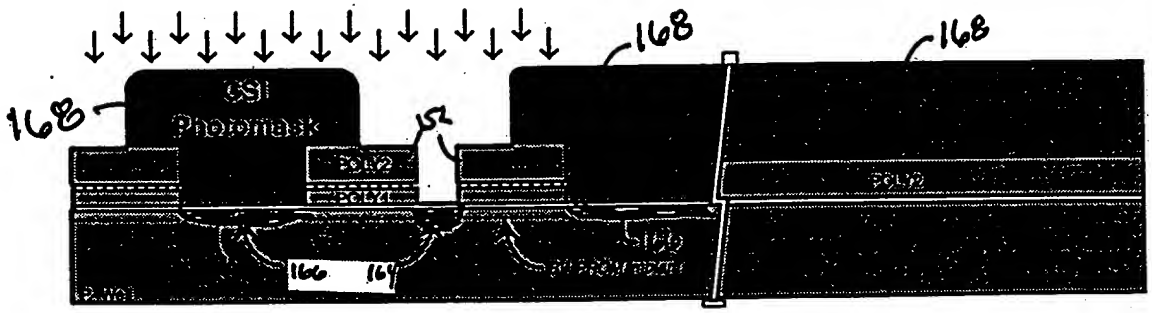
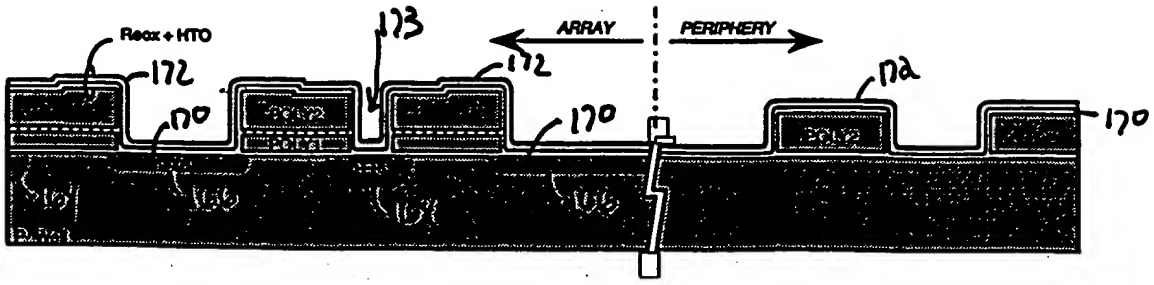


Fig. 23 (along Bit line direction)



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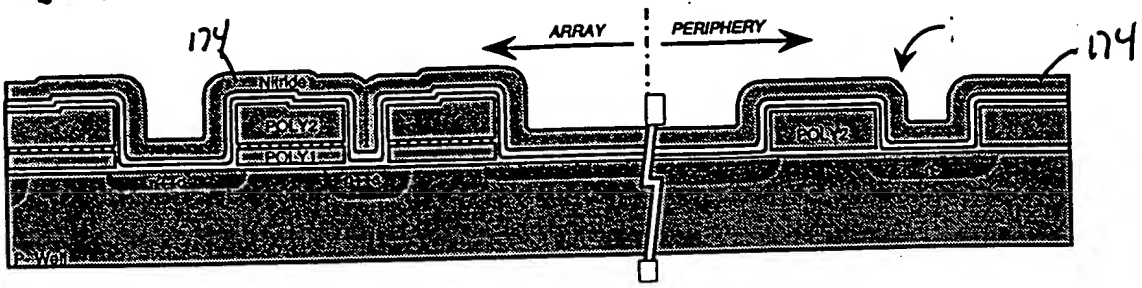

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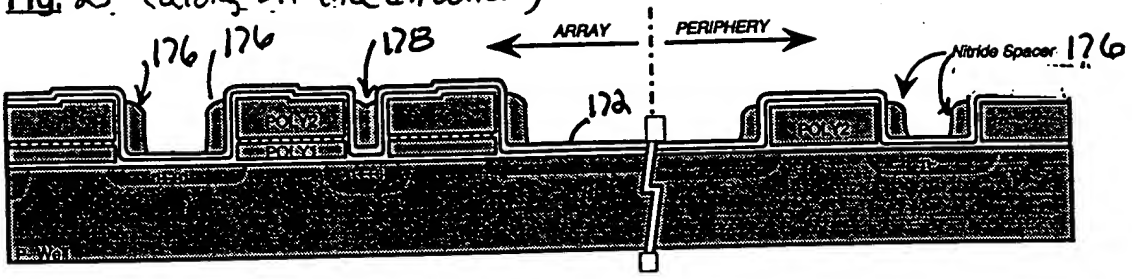


Fig. 26 (along bit line direction)

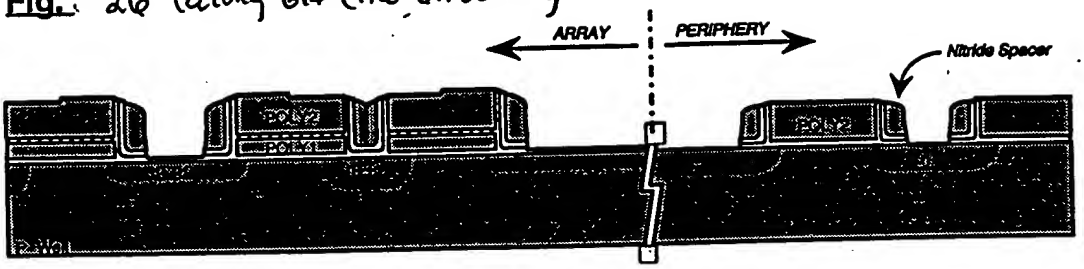
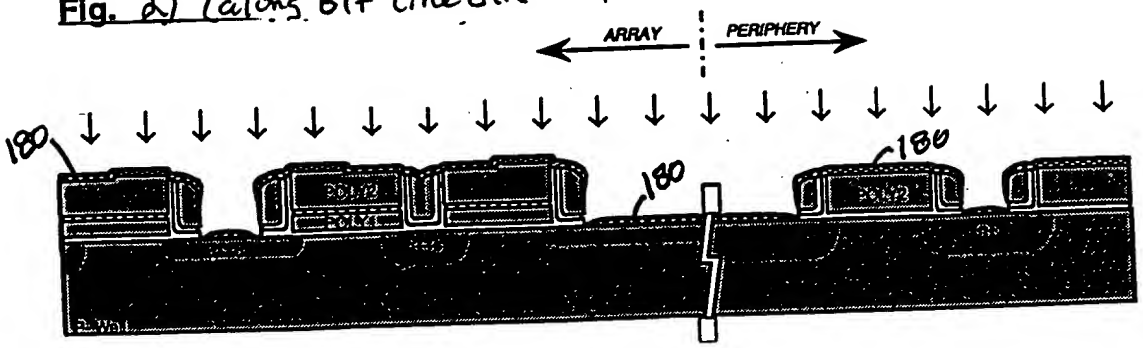


Fig. 27 (along bit line direction



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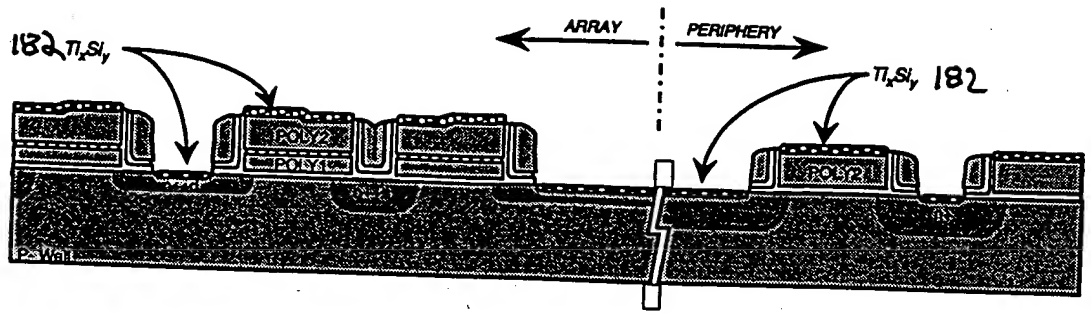

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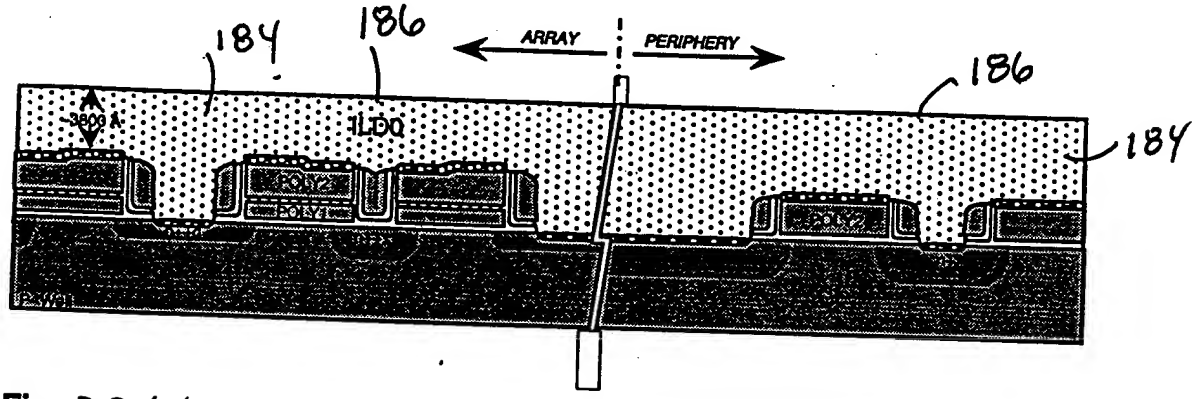

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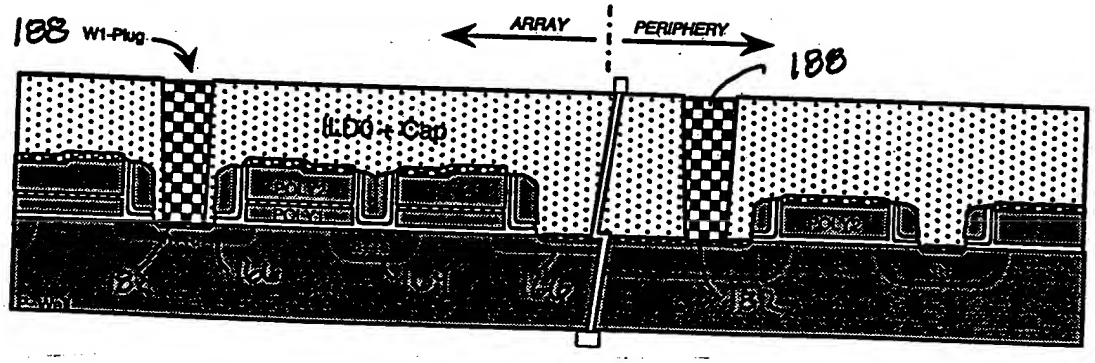
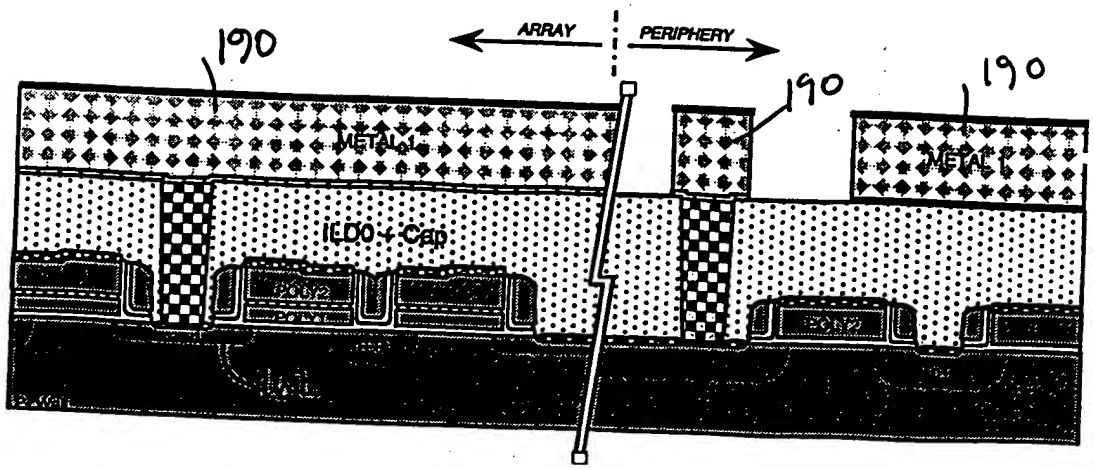
Fig. 31 (along bit line direction)